

24 OR 48 LINES INPUT OUTPUT LINES, PROGRAMMABLE DIRECTION, BUFFERED OUTPUTS

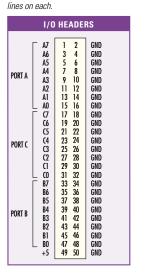
SPECIFICATIONS	
DIGITAL I/O	
Chip	82C55 (qty. 2)
Number of I/O lines	24 or 48
Direction	Programmable in groups of 4 or 8
Input voltage	Logic 0: 0.0V min, 0.8V max Logic 1: 2.0V min, Vcc max
Output voltage	Logic 0: 0.0V min, 0.55V max Logic 1: 2.0V min, Vcc -0.3V max
Output current	Logic 0: 64mA max per line Logic 1: 15mA max per line
Output drivers	74F245/74F243
Pull-up resistors	None
GENERAL	
Power supply	+5V ±10% @ 160mA typical
Temperature	0° to 70°C
PC/104 Bus	8 bits
Weight	-24: 2.7oz / 76g -48: 3.0oz /85g

Garnet-MM provides 24 or 48 digital I/O lines using 1 or 2 82C55 chips. It has buffered outputs for extra drive current capability to drive loads such as LEDs, small mechanical relays, or solid-state relays. The output buffers are controlled by onboard logic that monitors the 82C55 configuration registers and sets the buffer directions automatically. This board does not provide pull-up resistors on the I/O lines.

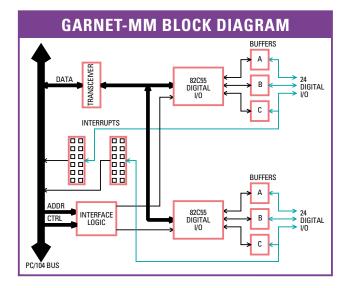
The board includes 2 interrupt lines that may be used for digital I/O data transfer at low rates (up to 1-2 KB/sec).



- 24 or 48 digital I/O lines using 1 or 2 82C55 chips
- Programmable port directions
- High-current outputs: -15/+64mA per line
- 2 PC interrupts
- Dual 50-pin I/O headers with 24 I/O lines each
- 0 to 70°C operation
- ◆ FREE Universal Driver software included



Garnet-MM has two identical digital I/O headers with 24 I/O



ORDERING GUIDE

GMM-24 24 Digital I/O, high-drive outputs
GMM-48 48 Digital I/O, high-drive outputs
For cables and accessories, see pages 46-47.